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PTO/SB/08A (10-88)
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Substitute for form 1448A/PTO		Complete if Known to <u>758,561</u>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	NOT YET ASSIGNED
		Filing Date	<u>1/14/04</u>
		First Named Inventor	<u>DICKINSON, et al</u>
		Group Art Unit	NOT YET ASSIGNED <u>2823</u>
		Examiner Name	NOT YET ASSIGNED <u>Andy</u>
Sheet <u>1</u> of <u>5</u>	Attorney Docket Number	<u>LUX - PO17</u>	

[illegible][illegible]

Examiner Signature	<i>B. Healy</i>	Date Considered	<i>9/8/05</i>
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO		Complete If Known <u>10/758,561</u>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	NOT YET ASSIGNED
		Filing Date	<u>7/19/04</u>
		First Named Inventor	<u>DICKINSON, et al</u>
		Art Unit	NOT YET ASSIGNED <u>2883</u>
		Examiner Name	NOT YET ASSIGNED <u>Healy</u>
Sheet <u>2</u>	of <u>5</u>	Attorney Docket Number	<u>LUX-PO17</u>

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
<u>BWH</u>	<u>1.</u>	"SCALING OPTOELECTRONIC-VLSI CIRCUITS INTO THE 21ST CENTURY: A TECHNOLOGY ROADMAP". ASHOK V. KRISHNAMOORTHY, MEMBER, IEEE, AND DAVID A.B. MILLER, FELLOW IEEE; IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS, VOL. 2, NO. 1, APRIL 1996	
<u>QMA</u>	<u>2.</u>	"OPTICAL INTERCONNECTS TO SILICON". DAVID A.B. MILLER IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS VOL. 6. NO. 6. NOVEMBER/DECEMBER 2000	
<u>BWH</u>	<u>3.</u>	"CHALLENGES IN OPTICALLY INTERCONNECTING ELECTRONICS" FRANK A.P. TOOLEY, MEMBER, IEEE. IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS VOL. 2, NO. 1, APRIL 1996.	
<u>BWH</u>	<u>4.</u>	"OPTICAL INTERCONNECTS: OUT OF THE BOX FOREVER?" DAWEI HUANG, MEMBER, IEEE, THERESA SZE, MEMBER, IEEE, ANDERS LANDIN, RICK LYTEL, MEMBER IEEE, AND HOWARD L. DAVIDSON, SENIOR MEMBER, IEEE	

Examiner Signature	<u>R. Healy</u>	Date Considered	<u>9/8/05</u>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

3

of

5

Application Number

Complete If Known 10/758,561

~~NOT YET ASSIGNED~~

Filing Date

1/14/04

First Named Inventor

Dickinson, et al

Art Unit

~~Not Yet Assigned~~ 2883

Examiner Name

~~Not Yet Assigned~~ Healy

Attorney Docket Number

LUX - P017

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
BUT 	5.	"VERY SHORT REACH OPTICAL INTERCONNECTS IN SYSTEMS: THE CASE FOR FIBER-TO-THE-PROCESSOR". The University of Southern California; http://www.usc.edu/alevi ; A.F.J. Levi; OPTICAL DIGITAL COMMUNICATION SEMINAR ON MAY 21, 2001.	
BUT 	6.	"HIGH-SPEED OPTOELECTRONIC VLSI SWITCHING CHIP WITH >4000 OPTICAL I/O BASED ON FLIP-CHIP BONDING OF MQW MODULATORS AND DETECTORS TO SILICON CMOS". Anthony L. Lentine, Keith W. Goossen, J. A. Walker Leo, Arthur D'Asaro, P. Hui, B. J. Tseng, R. E. Leibenguth, J. E. Cunningham, W. Y. Jan, Jen-Ming Kuo, D. W. Dahringer, D. P. Kossives	
BUT 	7.	HETEROGENEOUS INTEGRATION OF OE ARRAYS WITH Si ELECTRONICS AND MICROOPTICS: YUE LIU; IEEE TRANSACTIONS ON ADVANCED PACKAGING, VOL. 25, NO. 1, FEBRUARY 2002.	

Examiner Signature

B Healy

Date Considered

9/8/05

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet

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Application Number

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Filing Date

1/14/04

First Named Inventor

Dickinson, et al

Art Unit

~~Not Yet Assigned~~ 2883

Examiner Name

~~Not Yet Assigned~~ Healy

Attorney Docket Number

LUX-7017.

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BuH 1	8.	"FIBER WEIGHED FOR CHIP INTERCONNECT", WWW.CMPNET.COM. By ANTHONY CATALDO, OCT 12, 2001; URL: http://www.eetimes.com/story/OEG_20011012SO061	
BuH 1	9.	"EVALUATION OF ADVANCED OPTOELECTRONIC INTERCONNECT TECHNOLOGY" BY JANICE ONANIAN McMATHON; TOM EMBERLEY; MIT LINCOLN LABORATORY; 30 JULY 2001.	
BuH 1	10.	"PROSPECTS OF CMOS TECHNOLOGY FOR HIGH SPEED OPTICAL COMMUNICATION CIRCUIT". BEHZAD RAZAVI, SENIOR MEMBER, IEEE; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 37, NO. 9. SEPTEMBER 2002.	
BuH 1	11.	"ADVANCE IN SILICON-ON-INSULATOR OPTOELECTRONICS." B. JALALI, S. YEGHANARAYANAN, T. YOON, T. YOSHIMOTO, I. RENDINA, AND F. COPPINGER. IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS VOL. 4. NO. 6. NOVEMBER/DECEMBER 1998.	

Examiner
Signature

B. Healy

Date
Considered

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Sheet

5

of

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Complete If Known

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Application Number

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1/14/08

First Named Inventor

Dickinson, et al

Art Unit

~~Not yet Assigned~~ 2883

Examiner Name

~~Not yet Assigned Ideally~~

Attorney Docket Number

LUX-PO17

Examiner
Initials*Cite
No. 1

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 T^2

QWH

12.

"INTEGRATED OPTICS IN SILICON AND SiGe - HETEROSTRUCTURES"
Schüppert, et al ; JOURNAL OF LIGHTWAVE TECHNOLOGY
VOL. 14. NO. 10, OCTOBER 1996

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13.

"NANOTAPER FOR COMPACT MODE CONVERSION";
Almeida, et al; OPTICS LETTERS/VOL. 28 No. 15/AUG 1, 03

24A

14.

"ULTRA-LOW LOSS PHOTONIC INTEGRATED CIRCUIT WITH
MEMBRANE-TYPE PHOTONIC CRYSTAL WAVEGUIDES."
3 NOVEMBER 2003 / VOL. 11, No. 22 / OPTICS EXPRESS 2927.
SHARE J. McNAB, NIKOLAJ MOLL, AND YURI A VLASOV

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B-Henly

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